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(12) United States Patent

Davidson et al.

(54) SYSTEM AND METHOD FOR TOPOLOGY-AWARE JOB SCHEDULING AND BACKFILLING IN AN HPC ENVIRONMENT

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(56) References Cited

U.S. PATENT DOCUMENTS

4,868,818 A 9/1989 Madan et al. 4,885,770 A 12/1989 Croll (Continued)

FOREIGN PATENT DOCUMENTS

EP 0981089 A2 2/2000 EP 1737253 A1 12/2006 (Continued)

OTHER PUBLICATIONS

Keller et al., "Anatomy of a Resource Management System for HPC Clusters", 2001, Annual Review of Scalable Computing, vol. 3, pp. 1-23 *

(Continued)

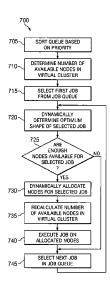
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(57) ABSTRACT

A method for job management in an HPC environment includes determining an unallocated subset from a plurality of HPC nodes, with each of the unallocated HPC nodes comprising an integrated fabric. An HPC job is selected from a job queue and executed using at least a portion of the unallocated subset of nodes.

16 Claims, 10 Drawing Sheets



US 9,189,275 B2Page 2

						- /	
(51)	Int. Cl.		,	6,996,674			Chiu et al.
	G06F 9/50		(2006.01)	7,016,299 7,028,228			Kashyap Lovy et al.
	G06F 11/14		(2006.01)	7,032,119		4/2006	
	G06F 11/20		(2006.01)	7,039,765		5/2006	
			,	7,039,827			Meyer et al.
(56) Referen			ces Cited	7,042,878		5/2006	
` ′				7,043,539 7,046,687			Treiber et al. Brown et al.
	U.S. I	PATENT	DOCUMENTS	7,051,185		5/2006	
	5 020 050 4	5/1001	G : 11	7,055,148			Marsh et al.
	5,020,059 A 5,280,607 A		Gorin et al. Bruck et al.	7,061,907			Hsieh et al.
	5,301,104 A		Yalamanchili	7,065,764			Prael et al. Oz et al.
	5,450,578 A		Mackenthun	7,073,053 7,093,004			Bernardin et al.
	5,459,857 A		Ludlam et al.	7,107,337			Barrow et al.
	5,495,474 A		Olnowich et al. Bruck et al.	7,127,597			Backman et al.
	5,513,313 A 5,521,591 A		Arora et al.	7,127,633			Olson et al.
	5,588,152 A		Dapp et al.	7,139,811 7,155,512			Lev Ran et al. Lean et al.
	5,603,044 A	2/1997	Annapareddy et al.	7,171,522			Watanabe et al.
	5,682,491 A		Pechanek et al.	7,185,062			Lolayekar et al.
	5,710,935 A 5,729,752 A		Barker et al. Snider et al.	7,207,039			Komarla et al.
	5,748,872 A		Norman	7,231,430			Brownell et al.
	5,748,882 A	5/1998		7,237,129 7,287,179		6/2007	Doyle et al.
	5,781,715 A	7/1998		7,299,334			Zohar et al.
	5,805,785 A		Dias et al.	7,299,377			
	5,872,928 A		Lewis et al. Thebaut et al.	7,340,555			Ashmore et al.
	5,889,953 A 5,926,619 A		Badovinatz et al.	7,379,983			Zaharias
	5,933,631 A		Mealey et al.	7,386,662 7,406.038			Kekre et al. Oelke et al.
	5,991,877 A		Luckenbaugh	7,400,038			Lortz et al.
	6,006,242 A		Poole et al.	7,433,931		10/2008	
	6,009,481 A	12/1999 2/2000	•	7,475,274			Davidson
	6,029,246 A 6,088,330 A		Bruck et al.	7,483,374			Nilakantan et al.
	6,158,010 A		Moriconi et al.	7,487,235 7,640,547			Andrews et al. Neiman et al.
	6,167,502 A		Pechanek et al.	7,644,153			Talwar et al.
	6,189,100 B1		Barr et al.	7,685,597			Czajkowski et al.
	6,230,252 B1 6,393,581 B1		Passint et al. Friedman et al.	7,711,977			Ballew et al.
	6,408,326 B1		Larsson et al.	8,145,837			Ballew et al.
	6,415,323 B1		McCanne et al.	8,160,061 8,190,714		4/2012 5/2012	Davidson et al.
	6,453,426 B1		Gamache et al.	8,335,909			Ballew et al.
	6,460,149 B1		Rowlands et al.	8,336,040		12/2012	Davidson et al.
	6,468,112 B1 6,477,663 B1		Follingstad et al. Laranjeira et al.	2001/0049740		12/2001	
	6,480,927 B1	11/2002		2002/0002613			Freeman et al.
	6,480,972 B1		Cromer et al.	2002/0059427 2002/0062454		5/2002	Tamaki et al.
	6,496,941 B1		Segal et al.	2002/0093950		7/2002	
	6,571,030 B1 6,587,921 B2		Ramaswami et al. Chiu et al.	2002/0103889			Markson et al.
	6,597,956 B1		Aziz et al.	2002/0133511			Hostetter et al.
	6,629,266 B1	9/2003	Harper et al.	2002/0159437 2003/0005039			Foster et al. Craddock et al.
	6,633,945 B1	10/2003		2003/0005039			French et al.
	6,643,764 B1		Thorson et al.	2003/0009551			Benfield et al.
	6,646,984 B1 6,658,504 B1		Mehra et al. Lieber et al.	2003/0018927			Gadir et al.
	6,675,264 B2		Chen et al.	2003/0046529			Loison et al. Gahan et al.
	6,683,696 B1		Urie et al.	2003/0084100 2003/0097487			Rietze et al.
	6,691,165 B1		Bruck et al.	2003/0097607			Bessire
	6,697,914 B1		Hospodor et al. Roselli et al.	2003/0135621			Romagnoli
	6,718,486 B1 6,735,660 B1		Osten et al.	2003/0154112			Neiman et al.
	6.748.437 B1		Mankude et al.	2003/0169734 2003/0188071			Lu et al. Kunjan et al.
	6,792,507 B2		Chiou et al.	2003/0188071			Bernardin et al.
	6,820,171 B1		Weber et al.	2003/0217105			Zircher et al.
	6,820,221 B2 6,853,388 B2		Fleming Ueno et al.	2003/0223361	A1	12/2003	Hussain et al.
	6,853,388 B2 6,918,051 B2		Block et al.	2003/0233427		12/2003	
	6,918,063 B2		Ho et al.	2003/0237018		12/2003	
	6,922,791 B2	7/2005	Mashayekhi et al.	2004/0024949 2004/0034794			Winkler et al. Mayer et al.
	6,941,396 B1		Thorpe et al.	2004/0034794			Romero
	6,944,785 B2 6,950,818 B2		Gadir et al. Dennis et al.	2004/0085897			Jacobi et al.
	6,950,818 B2 6,950,833 B2		Costello et al.	2004/0103218			Blumrich et al.
	6,952,766 B2		Dervin et al.	2004/0123033	A1	6/2004	Rudelic
	6,976,134 B1	12/2005	Lolayekar et al.	2004/0148376			Rangan et al.
	6,977,927 B1	12/2005	Bates et al.	2004/0186920	A1	9/2004	Birdwell et al.

(56) References Cited

U.S. PATENT DOCUMENTS

2004/0250031 A1 12/2004 Ji et al.	
2004/0260000 14 12/2004 D 1 1 1	
2004/0268000 A1 12/2004 Barker et al.	
2005/0015384 A1 1/2005 Wehrman et al.	
2005/0071843 A1 3/2005 Guo et al.	
2005/0149924 A1 7/2005 Komarla et al.	
2005/0173357 A1 8/2005 McClain et al.	
2005/0177670 A1 8/2005 Fujimoto et al.	
2005/0198200 A1 9/2005 Subramanian et al.	
2005/0235055 A1 10/2005 Davidson	
2005/0251567 A1 11/2005 Ballew et al.	
2005/0256942 A1 11/2005 McCardle et al.	
2006/0013207 A1 1/2006 McMillen et al.	
2006/0031940 A1 2/2006 Rozman et al.	
2006/0182440 A1 8/2006 Stefanov et al.	
2006/0195508 A1 8/2006 Bernardin et al.	
2007/0038749 A1 2/2007 Noya et al.	
2007/0067435 A1 3/2007 Landis et al.	
2007/0172235 A1 7/2007 Snider et al.	
2007/0253437 A1 11/2007 Radhakrishnan et al.	
2007/0299995 A1 12/2007 Hoese et al.	
2008/0101395 A1 5/2008 Ballew	
2008/0162732 A1 7/2008 Ballew	
2013/0304895 A1 11/2013 Davidson et al.	
2013/0311998 A1 11/2013 Davidson et al.	
2014/0040912 A1* 2/2014 Davidson et al 718/	104

FOREIGN PATENT DOCUMENTS

JP	05-274178	10/1993
JP	7200496	8/1995
JP	8227356	9/1996
JP	10-116261	5/1998
JP	10-222475	8/1998
JP	2002024192	1/2002
JP	2002-108839	4/2002
JP	2003-099412	4/2003
JP	2004110791	4/2004
JP	2006-065697	3/2006
JP	2006-190039	7/2006
JP	2007141305	6/2007
KR	2001-0000624	1/2001
WO	02084509	10/2002
WO	02095580	11/2002
WO	03005192	1/2003
WO	03005292 A1	1/2003

OTHER PUBLICATIONS

International Search Report and Written Opinion; International Application No. PCT/US2005/012500; Date of mailing: Aug 1, 2005; 12 pages.

Notice of Allowance for U.S. Appl. No. 10/991,598; mailed Jan. 22, 2008

Notice of Allowance for U.S. Appl. No. 10/991,598; mailed Apr. 18, 2008

Notice of Allowance for U.S. Appl. No. 11/107,4467; mailed Jul. 28, 2008

Office Action for U.S. Appl. No. 11/107,4467; mailed Mar. 7, 2008. Notice of Allowance; U.S. Appl. No. 12/246,786; Date of mailing: Oct. 17, 2011.

Notice of Allowance; U.S. Appl. No. 12/246,786; Date of mailing: Apr. 12, 2012.

Office Action for U.S. Appl. No. 12/246,783; mailed May 11, 2011. Canadian Intellectual Property Office; Office Action for Application No. 2,503,781; mailed Jan. 8, 2009; 4 pages.

Canadian Intellectual Property Office; Office Action for Application No. 2,503,781; mailed Jun. 6, 2011; 2 pages.

The Patent Office of the State Intellectual Property Office of the People's Republic of China, Office Action for Appl. No. 200510087855.3, mailed Aug. 3, 2012; 8 pages (with Translation). The Patent Office of the State Intellectual Property Office of the People's Republic of China, Office Action for Appl. No. 200510087855.3, mailed Nov. 20, 2012; 8 pages (with Translation).

EPO Communication, Netherlands Communications re: Summons to attend oral proceedings in accordance with Rule 115(1) EPC for Application No. 05 252 240.6; mailed Aug. 5, 2011; 8 pages.

Israel Patent Office Communication Re: Brief Translation of the Office Action, Re: Notice of Deficiencies in Patent No. 178610, mailed Sep. 20, 2010, 4 pages.

Translation of Office Action, Japanese Patent Application No. 2005-117406, 1 page, Mailed Jun. 26, 2009.

Translation of Office Action, Japanese Patent Application No. 2005-117406, 6 pages, Mailed Dec. 28, 2007.

Korean Patent Office Communication; Korean OA and Translation for Appl. No. 10-2006-7021323, dated Jul. 14, 2011; 8 pages.

Perbadanan Harat Intelek Malaysia; Malaysia Patent Office Communication re: Substantive Examination Adverse Report (Section 30(1)/30(2)) for Appl. No. PI 20051526, mailed Jan. 31, 2011, 3 pages.

Communication from the EPO, European Search Report for Appl. PCT/US2005/012489 and Written Opinion of the International Search Authority, mailed Nov. 18, 2005, 14 pages.

IP Office of Singapore, Examination Report, Appl. No. 200607088-2, mailed Apr. 13, 2009, 7 pages.

USPTO OA for U.S. Appl. No. 10/991,994, filed Nov. 17, 2004, mailed Mar. 20, 2008, 16 pages.

USPTO OA for U.S. Appl. No. 10/991,994, filed Nov. 17, 2004, mailed Mar. 26, 2009, 14 pages.

USPTO OA for U.S. Appl. No. 10/991,994, filed Nov. 17, 2004, mailed Aug. 4, 2010, 11 pages.

USPTO OA for U.S. Appl. No. 10/991,994, filed Nov. 17, 2004, mailed Sep. 19, 2011, 15 pages.

USPTO OA for U.S. Appl. No. 10/991,994, filed Nov. 17, 2004, mailed Sep. 12, 2008, 16 pages.

USPTO OA for U.S. Appl. No. 10/991,994, filed Nov. 17, 2004, mailed Oct. 15, 200, 14 pages.

Communication from the EPO, European Search Report for Appl. PCT/US2005/012242 and Written Opinion of the International Search Authority, mailed Sep. 19, 2005, 16 pages.

USPTO Notice of Allowance for U.S. Appl. No. 10/991,754, filed Nov. 17, 2004, mailed Sep. 9, 2008, 8 pages.

USPTO OA for U.S. Appl. No. 10/991,754, filed Nov. 17, 2004, mailed Mar. 20, 2008, 29 pages.

International Search Report and Written Opinion; International Application No. PCT/US2007/0081722; Date of mailing: May 30, 2008; 13 pages.

EPO, Communication Pursuant to Article 34(3)EPC, Mailed Dec. 16, 2010, Application No. 07 865 807.7-1249, 7 pages.

Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration for the International Application No. PCT. US2007087947; mailed May 7, 2008; 11 pages.

USPTO OA for U.S. Appl. No. 11/618,196, filed Dec. 29, 2006, mailed Feb. 3, 2011, 16 pages.

USPTO OA for U.S. Appl. No. 11/618,196, filed Dec. 29, 2006, mailed May 13, 2009, 10 pages.

USPTO OA for U.S. Appl. No. 11/618,196, filed Dec. 29, 2006, mailed Jul. 18, 2011, 20 pages.

USPTO OA for U.S. Appl. No. 11/618,196, filed Dec. 29, 2006, mailed Dec. 16, 2009, 8 pages.

Communication Pursuant to Article 94(3) EPC for Application No. 08 713 443.3-2224 from EPO dated Nov. 5, 2009; 9 pages.

PCT; International Search Report and Written Opininion; for PCT/US2008/050086; mailed Jan. 3, 2008; 15 pages.

Masaaki et al.; Abstract of JP8227356, 1 page, published Sep. 3, 1996.

Advanced Micro Devices et al.; "Hypertransport Technology I/O Link—a High-Bandwidth I/O Architecture," Jul. 20, 2001, pp. 1-25. Baraglia et al.; RsdEditor: A Graphical User Interface for Specifying Metacomputer Components, Heterogeneous Computing Workshop, 2000, pp. 336-345.

Bhanot et al.; "The BlueGene/L Supercomputer," 20th International Symposium on Lattice Field Theory, vol. 119, Jun. 2002, 8 pages.

Allen et al.; "The Cactus Worm: Experiments with Dynamic Resource Discovery and Allocation in a Grid Environment," 16 pages, Jan. 8, 2001.

(56) References Cited

OTHER PUBLICATIONS

Chang et al.; "Performance Improvement of Allocation Schemes for Mesh-Connected Computers;" Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, vol. 52, No. 1, Jul. 10, 1998; pp. 40-68.

Hsing-Lung Chen et al.; Distributed Submesh Determination in Faulty Tori and Meshes; XP-10216762; 1997; pp. 65-70.

Choo et al.; "Processor Scheduling and Allocation for 3D Torus Muticomputer Systems," IEEE Transactions on Parallel and Distributed Systems; vol. 11, No. 5, May 2000, pp. 475-484.

Cisco Systems; Cisco 12012 Gigabit Switch Router Switch Fabric Cards Replacement Instructions; Doc. No. 78-4343-02; 14 pages. Unknown, "CISCO Local Director Configuration and Command Reference Guide," Internet Article, Online, Apr. 4, 2001.

Cukier, M. et al.; "AQUA: an Adaptive architecture that provides dependable distributed objects;" 1998 Reliable Distributed Systems. Proceedings. 17th IEEE Symposium on West Lafayette, USA Oct. 20, 1998, pp. 245-253, XP010319083, DOI: DOI: 10.1109/RELDIS. 1998.740506 ISBN: 978-0-8186-9218.

Culler et al.; "Parallel Computer Architecture—A Hardware/Software Approach, Interconnection Network Design," Morgan Kaufmann, 1999, 30 pages.

Di Santo et al.; "Kernel Implementation of Locality-Aware Dispatching Techniques for Web Server Clusters," Cluster Computing, 2003 Proceedings, pp. 154-162.

"MS-8X; 8-Port Modular SCL Switch; for clustering, SANs, networking and bus bridging," Dolphin Interconnect Solutions, XP-002480478, Jan. 2002, 2 pages.

"Installation Guide for Dolphin PCI-SCI Adapters," Dolphin Interconnect Solutions, XP-02480479; Sep. 6, 2006; 20 pages.

EPO Registered Letter, Application No. 05 252 239.8-1243, 6 pages; mailed Feb. 2, 2007.

Falck et al.; Swedish Patent No. 102405; 4 pages; Aug. 26, 1941. Feitelson, "Job Scheduling in Multiprogrammed Parallel Systems," IBM Research Report, Aug. 1997, pp. 1-90.

L. G. Harbaugh et al.; "Building High-Performance Linux Clusters, Sponsored by Appro," XP-002480128; Jun. 2004; 23 pages.

John P. Hayes et al.; Hypercube Supercomputers; Proceedings of the IEEE; vol. 77, No. 12; Dec. 1989; XP009113537; pp. 1829-1271. Haynes et al.; "A Visualization Toold for Analyzing Cluster Performance Data," 42 Annual Symposium on Foundations of Computer Science, (FOCS 2001), Las Vegas, Oct. 14-17, 2001, 8 pages.

Hans-Ulrich Heiss, "Processor Management in Two-Dimensional Grid Architecutres," Interner Bericht Nr. Dec. 1992, XP002416087, Dec. 1992, 51 pages.

Matthias Hovestadt et al; Scheduling in HPC Resource Mangement Systems: Queing vs. Planning; Proceedings of the 9th Workshop on Job Scheduling Strategies for Parallel Processing, Seattle, WA, 2003; pp. 1-19.

Anonymous, "HP AlphaServer SC User Guide;"Internet Article, Online, XP002336777; retrieved from the internet on Jul. 12, 2004 from: http://web1.quadrics.com/onlinedocs/AlphaServer/Eagle/html/AlphaServerUserGuide.

Jackson et al.; "Core Algorithms of Maui Scheduler," 2001; Springer-Verlad, Berlin Heidelberg, pp. 89-102.

Jonsson et al.; "Comparative Performance of InfiniBand Architecture and Gigabit Ethernet Interconnects on Intel Itanium 2 Microarchitecture-based Clusters;" May 2003, 4th European LS-DYNA Users Conference, 10 pages.

Kandlur et al.; "Hypercube Management in the Presence of Node Failures," Third Conference on Hypercube Concurrent Computers and Applications ACM New York, vol. 1; 1988, pp. 328-336.

Moonsoo Kang et al; Job-Based Queue Delay Modeling in a Space-Shared Hypercube; XP-10356056; 1999; pp. 313-318.

Keller et al.; "Scheduling in HPC Resource Management Systems: Queing vs. Planning;" Jun. 2003, Proceedings of the 9th Workshop on Job Scheduling Strategies for Parallel Processing, Seattle, WA, pp. 1-19.

Geunmo Kim et al.; On Submes Allocation for Mesh Multicomputers: a Best-Fil Allocation and a Virtual Submesh Allocation for Faulty

Meshes; IEEE Transactions on Parallel and Distributed Systems; vol. 9; No. 2; Feb. 1998; XP-000736328; pp. 175-185.

Krevat et al.; "Job Scheduling for the BlueGene/LSystem," Lecture Notes in Computer Science, vol. 2537, pp. 38-54, XP00233643, Jul. 24, 2002.

Liu et al.; "Non-Contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers;" Supercomputing '94, Proceedings, Washington D.C., Nov. 144-18, 1994; pp. 227-236.

Look up Tech Terms—Switching Fabric; SearchStorage.com; http://search.techtarget.com; retreived Oct. 6, 2009; 4 pages.

Ma et al.; E-Kernal: An Embedding Kernel on the IBM Victor V256, Multiprocessor for Program Mapping and Network Reconfiguration, IEEE Transactions on Paralle and Distributed Systems, IEEE Service Center, vol. 5, No. 9, Sep. 5, 1994, pp. 977-994.

Moore et al.; "Managing Mixed-Use Clusters with Clusters-onDemand," Internet Article, Nov. 2002.

Anonymous, "Message Passing Interface (MPI);" retreived on Jul. 18, 2005; from Internet http://webarchive.org/web/20040102194825 / http://www.llnl.gov.computing/tutorials/mpi/>, pp. 1-33, 103, 25 & 26.

Panagiotis et al.; "inetd—Internet Services Daemon," pp. 1-4, 1994 Man-cgi 1.158, 1995 Modified for Solaris 2.3.

Patel et al.; "Sage: An Application Development Toold Suite for High Performance Computing Systems," Aerospace Conference Proceedings, 2000, IEEE Mar. 18-25, 2000 pp. 491-500.

Pinkston et al.; "InfiniBand: The "De Facto" Future Standard for System and Local Area Networks or Just a Scalable Replacement for PCI Buses?;" Clustering Computing-Kluwer Academic Publishers, vol. 6, No. 2, 2003, pp. 95-104.

Qiao et al.; "Efficient Processor Allocation for 3D Tori," Paralle Processing Sysmposium 1995, Proceedings, 9th International, Apr. 25-28, 1995, IEEE Comput. Soc., pp. 466-471, 921.

Ross et al.; "3.3 Connectionless Transport;" Feb. 22, 2001; pp. 1-4; retrieved on Nov. 1, 2005.

Rzymianowicz et al., "Clustering SMP Nodes with the ATOLL Network: A Look into the Future of System Area Networks;" Proceedings of High Performance Computing, 8th International Conference, May 8, 2000, 10 pages.

Stefanie Meier; "Konzept eines Accounting-und Kontingetierungssystems für den Einsatz in einem wissenschaftlichen Rechenzentrum;" Jun. 11, 2002; XP55031256; [retrieved from the internet] www2.fz-juelich.de/zam/docs/printable/ib/ib-02/ib-2002-08.pdf; 64 pages.

Shiraishi et al.; Parallel Job Execution Tool: ParaJET; Institute of Electronics, Information and Communication Engineers, Technical Report of IEICE, CPSY95-60 Aug. 1996; 9 Pages.

Kurt Windisch et al.; ProcSimity: An Experimental Tool for Processor Allocation and Scheduling in Highly Parallel Systems; XP-101-30254; 1995; pp. 414-412.

Wong; "Switch-Chip Fuels Third-Generation InfiniBand," Nov. 10, 2003, Electronic Design, 2 pages.

Hee Yong Youn et al.; Dynamic Task Scheduling and Allocation for 3D Torus Multicomputer Systems; 1996 International Conference on Parallel Processing; XP-9113574; pp. III-199-III-206.

USPTO OA for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Oct. 4, 2006, 15 pages.

Canadian Intellectual Property Office, Office Action for Appl. No. 2,503,773; mailed Jun. 4, 2009; 3 pages.

Canadian Intellectual Property Office; Office Action for Application No. 2,503,773; mailed Aug. 9, 2011; 2 pages.

The Patent Office of the State Intellectual Property Office of the People's Republic of China, Office Action for Appl. No. 200510081719.3, mailed Apr. 20, 2007; 11 pages.

EPO Communication Pursuant to Article 94(3) EPC for Application 05 252 234.9; mailed Aug. 14, 2009; 3 pages.

Israel Patent Office Communication Re: Brief Translation of the Office Action, Re: Notice of Deficiencies in Patent No. 178607, mailed Sep. 13, 2010; 4 pages.

Government of India Patent Office; First Examination Report for Application 6323/DELNP/2006; mailed Oct. 4, 2013; 2 pages.

Translation of an Office Action of Japanese Patent Office, Appl. No. 2005/117402. mailed Jan. 15, 2008, 6 pages.

(56)References Cited

OTHER PUBLICATIONS

Malaysia Patent Office Communication re: Substantive Examination Adverse Report (Section 30(01)/30(2)) for Appl. No. PI 20051525, Filing Date Apr. 5, 2005, mailed Aug. 30, 2010, 2 pages.

Communication from the EPO, European Search Report for Appl. PCT/US2005/012316 and Written Opinion of the International Search Authority, mailed Sep. 14, 2005, 11 pages.

USPTO Notice of Allowance for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Oct. 27, 2011, 10 pages.

USPTO OA for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Mar. 19, 2010, 22 pages.

USPTO OA for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Apr. 15, 2011, 22 pages.

USPTO OA for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Apr. 17, 2009, 13 pages.

USPTO OA for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Jul. 23, 2008, 12 pages.

USPTO OA for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Oct. 27, 2010, 18 pages.

USPTO OA for U.S. Appl. No. 10/825,345, filed Apr. 15, 2004, mailed Sep. 16, 2009, 15 pages.

Canadian Intellectual Property Office; Office Action for Application No. 2,503,775; mailed Jan. 8, 2009; 5 pages.

EPO Communication Pursuant to Article 94(3) EPC for Application 07 007 897.7; mailed Feb. 2, 2011; 7 pages.

EPO Communication Pursuant to Article 94(3) EPC for Application 07 007 897.7; mailed Sep. 18, 2009; 1 page.

Communication from the EPO, European Search Report for Appl. EP 05 25 2235, mailed Jul. 22, 2005, 3 pages.

EPO Communication for Appl. No. 07007897.1; mailed Mar. 26, 2006; 12 pages.

Communication from the EPO, Communication pursuant to Article 94(3) EPC; Application No. EP 07 007 897.7-2211, mailed Jul. 5, 2012, 7 pages.

Israel Patent Office Communication Re: Brief Translation of the Office Action, Re: Notice of Deficiencies in Patent No. 178608, mailed Sep. 20, 2010, 4 pages.

Government of India Patent Office; First Examination Report for Application 632/DELNP/2006; mailed Oct. 4, 2013; 2 pages

Translation of Office Action, Japanese Patent Application No. 2005-117403, 1 page, Mailed Jun. 26, 2009.

Translation of Office Action, Japanese Patent Application No. 2005-117403, 4 pages, Mailed Dec. 28, 2007.

Korean Office Action and English Translation for Application No. 10-2006-7023882, mailed Jul. 18, 201; 4 pages.

Perbadanan Harta Intelek Malaysia; Office Action for Application No. PI 20051531; mailed Oct. 31, 2008; 4 pages.

Offical Letter received Apr. 15, 2010, re: ROC (Taiwanese) Appl. No. 94111492, notifying of contents of forthcoming second (final) Office Action and indicating opportunity to respond, and Search Report of the EP corresponding application (EP 1566738 A3), 7 pages.

The Intellectual Property Bureau Ministry of Economic Affairs, Office Action for Appl. No. 94111492, 2 pages.

USPTO Ex Parte OA for U.S. Appl. No. 10/824,874, filed Apr. 15, 2004, mailed Sep. 4, 2009, 5 pages.

USPTO Notice of Allowance for U.S. Appl. No. 10/824,874, filed

Apr. 15, 2004, mailed Feb. 8, 2010, 5 pages. USPTO Notice of Allowance for U.S. Appl. No. 10/824,874, filed

Apr. 15, 2004, mailed May 5, 2011, 5 pages USPTO Notice of Allowance for U.S. Appl. No. 10/824,874, filed

Apr. 15, 2004, mailed Aug. 2, 2011, 7 pages. USPTO Notice of Allowance for U.S. Appl. No. 10/824,874, filed

Apr. 15, 2004, mailed Sep. 20, 2010, 10 pages. USPTO OA for U.S. Appl. No. 10/824,874, filed Apr. 15, 2004,

mailed Jun. 5, 2006, 17 pages. USPTO OA for U.S. Appl. No. 10/824,874, filed Apr. 15, 2004, mailed Feb. 26, 2007, 13 pages.

USPTO OA for U.S. Appl. No. 10/824,874, filed Apr. 15, 2004, mailed Jul. 11, 2008, 13 pages.

USPTO OA for U.S. Appl. No. 10/824,874, filed Apr. 15, 2004, mailed Jul. 31, 2007, 13 pages.

USPTO FOA for U.S. Appl. No. 13/712,451, filed Dec. 12, 2012, mailed Dec. 19, 2013, 13 pages.

U.S. Appl. No. 13/712,451; Non-Final Office Action; Date Filed: Dec. 12, 2012; Date Mailed: May 29, 2013; pp. 1-9.

European Patent Office Communication Re: Summons to attend oral proceedings with Rule 115(1)EPC, for Application No. 05 732 940.1, mailed Jan. 27, 2011; 7 pages.

European Patent Office Communication, Communication Pursuant to Article 94(3)EPC, for Application No. 05 732 940.1, mailed Jan. 15, 2010; 4 pages.

European Patent Office Communication, Munich Germany: Communication re: Minnutes of oral proceedings in Accordance with Rule 124(4)EPC, for Application No. 05 732 940.1, mailed Jul. 13, 2011; 14 pages.

Government of India Patent Office; First Examination Report for Application 6020/DELNP/2006; mailed Oct. 30, 2013; 2 pages.

Translation of Japanes Office Action for Appl. No. 2007-508457, dated Aug. 12, 2009, 3 pages.

Japanese Patent Office Communication, Japanes Office Action and Translation of Office Action for Appl. No. 2007-508457, dated Sep. 29, 2010, 12 pages.

U.S. Appl. No. 10/825,539; Final Office Action; Date Filed: Apr. 15, 2004; Date Mailed: Dec. 26, 2013; pp. 1-12.

USPTO OA for U.S. Appl. No. 10/825,539, filed Apr. 15, 2004, mailed Jan. 10, 2008, 18 pages.

USPTO OA for U.S. Appl. No. 10/825,539, filed Apr. 15, 2004, mailed Jan. 28, 2010, 19 pages.

USPTO OA for U.S. Appl. No. 10/825,539, filed Apr. 15, 2004,

mailed Mar. 17, 2011, 22 pages. U.S. Appl. No. 10/825,539; Non-Final Office Action; Date Filed: Apr. 15, 2004; Date Mailed: May 28, 2013; pp. 1-27.

USPTO OA for U.S. Appl. No. 10/825,539, filed Apr. 15, 2004, mailed Jun. 25, 2009, 11 pages.

USPTO OA for U.S. Appl. No. 10/825,539, filed Apr. 15, 2004, mailed Aug. 9, 2010, 22 pages.

USPTO OA for U.S. Appl. No. 10/825,539, filed Apr. 15, 2004, mailed Nov. 13, 2008, 16 pages.

European Patent Office, Munich, Germany; Communications re: Summons to attend oral proceedings in accordance to Rule 115(1) EPC for Application No. 05 742 298.2; mailed May 13, 2011; 7 pages.

Japanese Office Action and English Translation for Appl. No. 2007-508456, dated Aug. 10, 2010; 6 pages.

Communication from the EPO, European Search Report for Appl. PCT/US2005/012313 and Written Opinion of the International Search Authority, mailed Sep. 20, 2005, 14 pages.

USPTO Notice of Allowance for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Feb. 11, 2009, 5 pages

USPTO Notice of Allowance for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed May 12, 2009, 4 pages.

USPTO Notice of Allowance for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Aug. 29, 2009, 5 pages.

USPTO Notice of Allowance for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Dec. 17, 2009, 5 pages.

USPTO OA for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Jan. 29, 2008, 17 pages.

USPTO OA for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Mar. 12, 2007, 12 pages.

USPTO OA for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Oct. 4, 2007, 15 pages.

USPTO OA for U.S. Appl. No. 10/826,959, filed Apr. 15, 2004, mailed Oct. 20, 2008, 17 pages.

European Patent Office, Netherlands, Communications re: Summons to attend oral proceedings in accordance to Rule 115(1) EPC for

Application No. 05 737 440.7; mailed Aug. 4, 2011; 7 pages European Patent Office, Communications Pursuant to Article 94(3) EPC for Application No. 05 737 440.7; mailed Nov. 12, 2009; 4

Japanese Office Action and English Translation for Appl. No. 2007-508520, dated Jun. 17, 2011; 6 pages.

(56) References Cited

OTHER PUBLICATIONS

Communication from the EPO, European Search Report for Appl. PCT/US2005/012643 and Written Opinion of the International Search Authority, mailed Apr. 19, 2006, 11 pages.

USPTO OA for U.S. Appl. No. 10/824,873, filed Apr. 15, 2004, mailed Jan. 14, 2010, 13 pages.

USPTO OA for U.S. Appl. No. 10/824,873, filed Apr. 15, 2004, mailed Jun. 25, 2009, 11 pages.

USPTO OA for U.S. Appl. No. 10/824,873, filed Apr. 15, 2004, mailed Nov. 17, 2008, 14 pages.

USPTO OA for U.S. Appl. No. 10/824,873, filed Apr. 15, 2004, mailed Nov. 18, 2011, 14 pages.

USPTO OA for U.S. Appl. No. 10/824,873, filed Apr. 15, 2004, mailed Dec. 4, 2007, 10 pages.

Israel Patent Office Communication Re: Hebrew version and brief English Translation of Office Action, for Patent No. 178606, mailed Sep. 19, 2010; 3 pages.

Government of India Patent Office; First Examination Report for Application 6368/DELNP/2006; mailed Nov. 4, 2013; 2 pages. Translation of Office Action, Japanese Patent Application No. 2005-117404, 1 page, Mailed Jun. 26, 2009.

Japanes Office Action and English Translation for Japanese Patent Application No. 2005-117404, 6 pages, Mailed Dec. 7, 2010.

Korean Notice of Last Preliminary Rejection for Application No. 10-2006-7023880; mailed May 25, 2009; 5 pages.

English Translation of the Korean Notice of Last Preliminary Rejection for Application No. 10-2006-7023880; mailed May 28, 2009; 4 pages.

Intellectual Property Corporation of Malaysia; Substantive Examination Adverse Report for Application PI 20051527; mailed Jan. 31, 2013; 4 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed Feb. 11, 2008, 10 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed Apr. 15, 2009, 10 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed Apr. 30, 2010, 9 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed May 7, 2007, 9 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed May 25, 2011, 10 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed Oct. 15, 2005, 12 pages.

USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed Oct. 15, 2010, 13 pages.

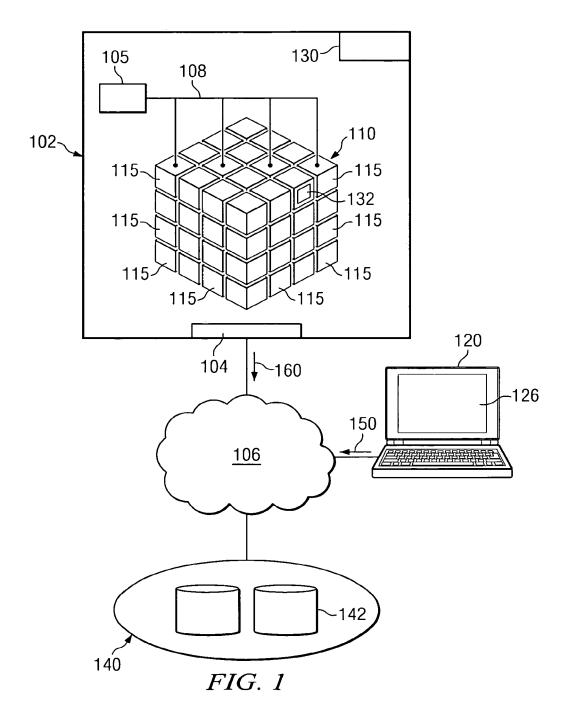
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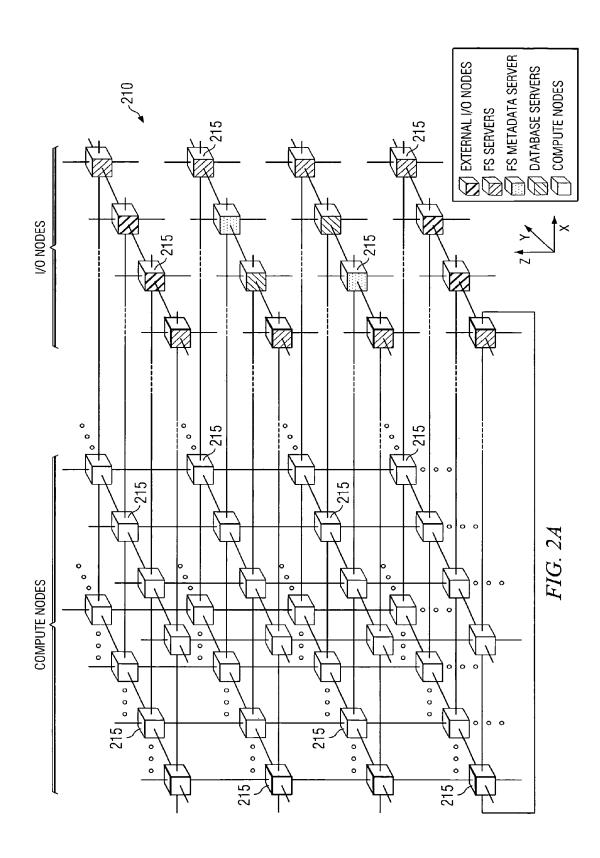
USPTO OA for U.S. Appl. No. 10/825,021, filed Apr. 15, 2004, mailed Nov. 10, 2011, 10 pages.

USPTO OA for U.S. Appl. No. 14/052,093, filed Oct. 11, 2013, mailed Dec. 20, 2013, 12 pages.

European Patent Office Communication Re: Decision to Refuse European Patent Application, No. 05 252 239.8-1243, at the oral proceedings date Mar. 11, 2010, Ref. JL 5105, and the minutes in accordance with Rule 124(4) EPC (63 pages).

^{*} cited by examiner





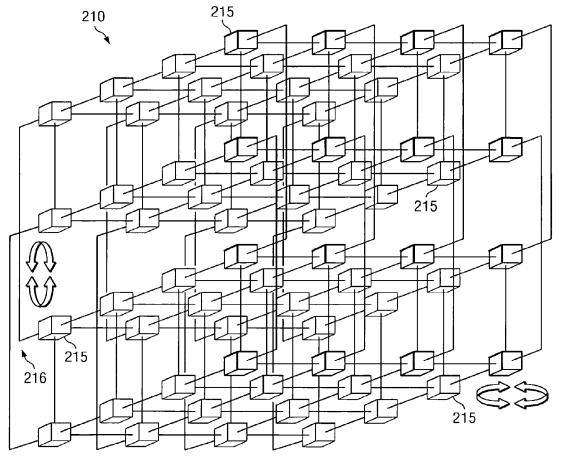


FIG. 2B

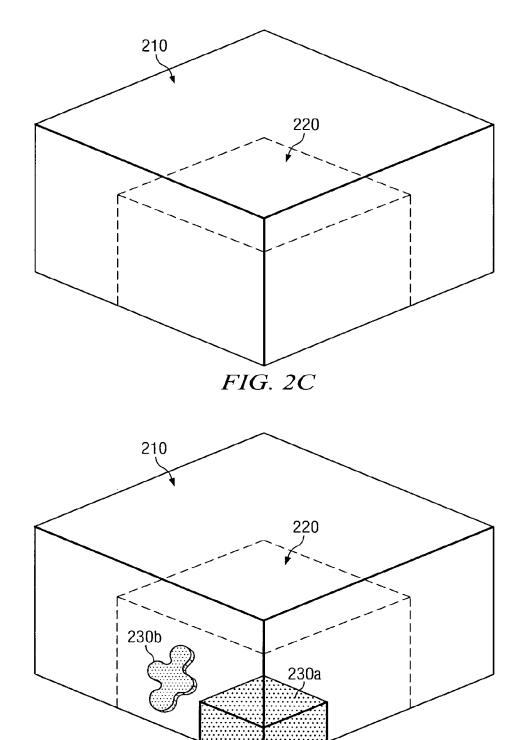
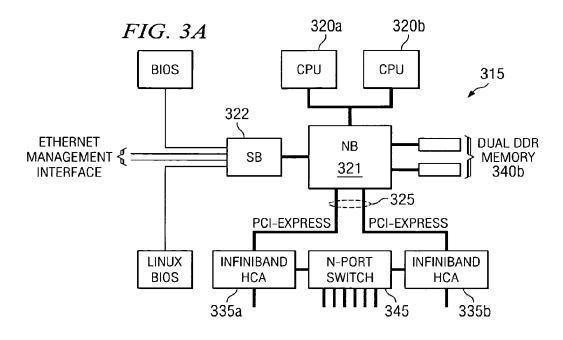
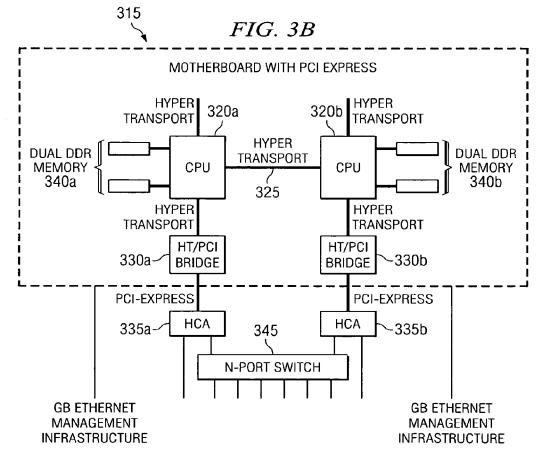
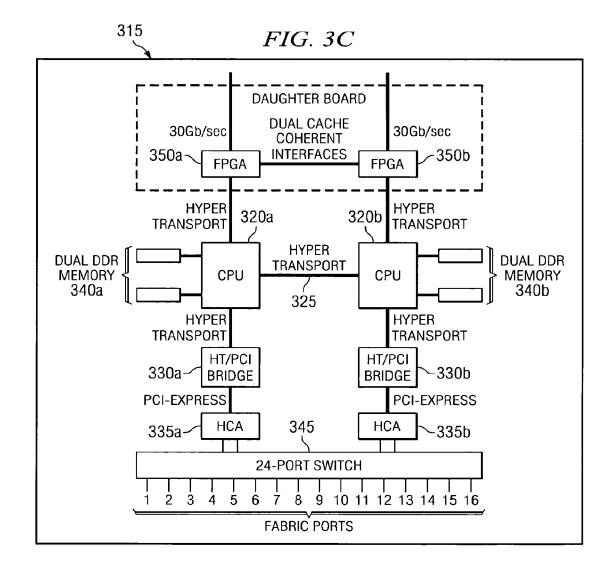


FIG. 2D







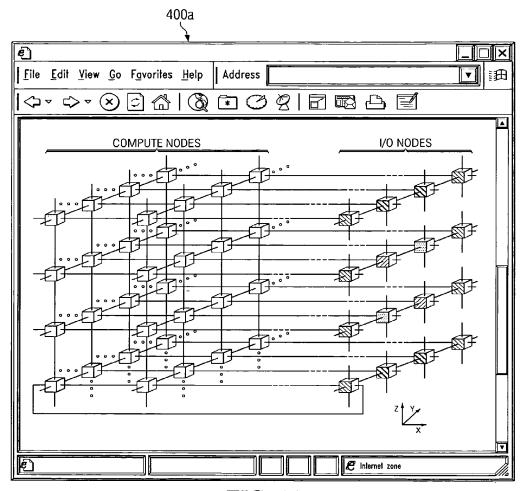
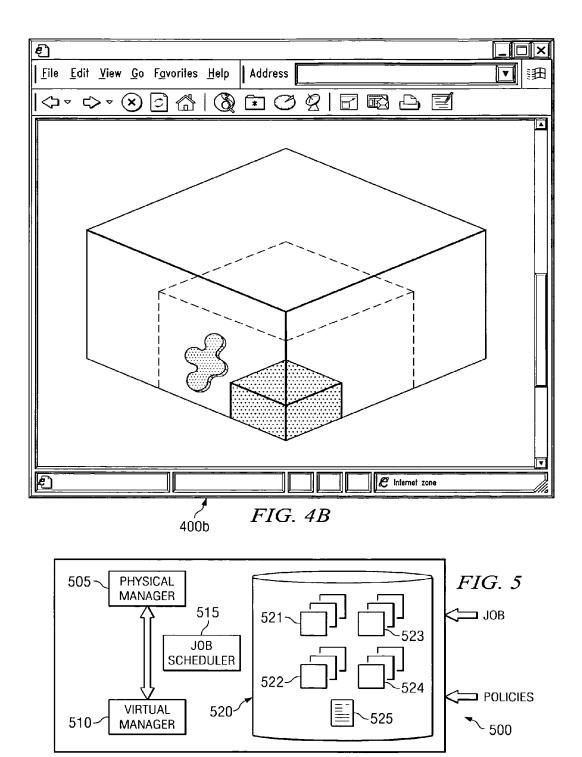
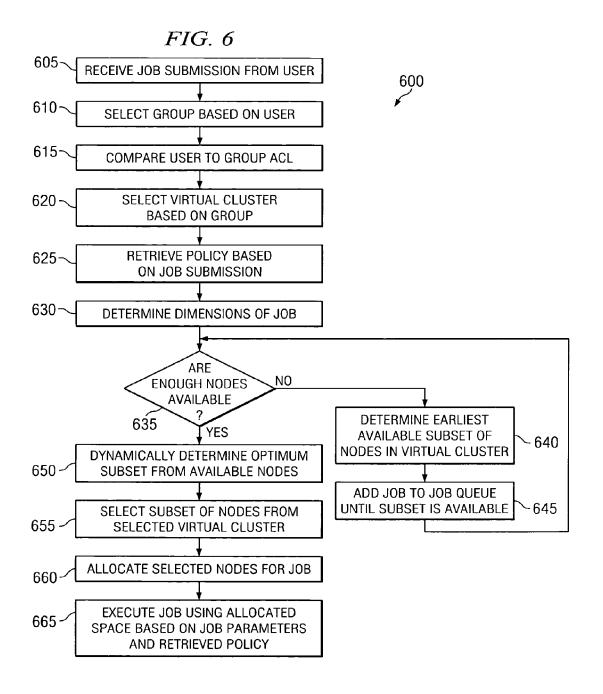
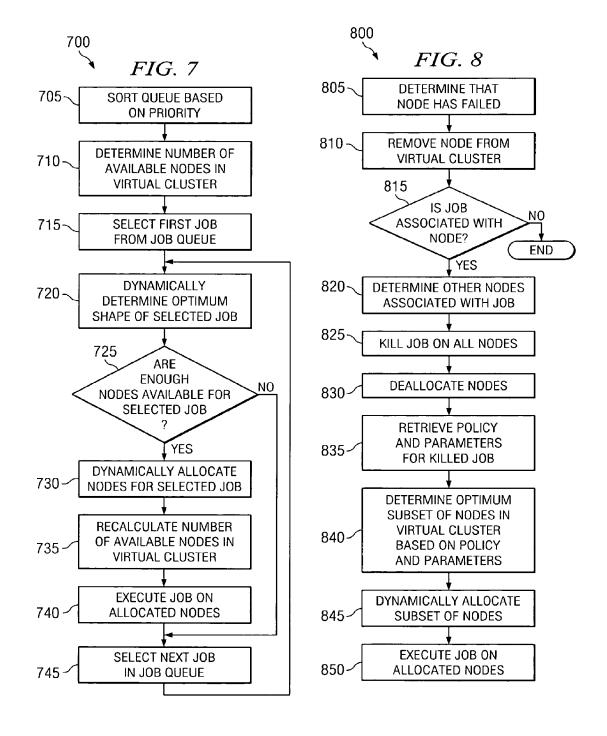


FIG. 4A







SYSTEM AND METHOD FOR TOPOLOGY-AWARE JOB SCHEDULING AND BACKFILLING IN AN HPC ENVIRONMENT

CROSS-REFERENCE TO RELATED APPLICATION

This application is a continuation of U.S. patent application Ser. No. 10/825,021, filed Apr. 15, 2004, now U.S. Pat. No. 8,366,040, the disclosure of which is incorporated by reference herein in its entirety.

TECHNICAL FIELD

This disclosure relates generally to the field of data processing and, more specifically, to a system and method for topology-aware job scheduling and backfilling in an HPC environment.

BACKGROUND OF THE INVENTION

High Performance Computing (HPC) is often characterized by the computing systems used by scientists and engineers for modeling, simulating, and analyzing complex physical or algorithmic phenomena. Currently, HPC 25 machines are typically designed using numerous HPC clusters of one or more processors referred to as nodes. For most large scientific and engineering applications, performance is chiefly determined by parallel scalability and not the speed of individual nodes; therefore, scalability is often a limiting 30 factor in building or purchasing such high performance clusters. Scalability is generally considered to be based on i) hardware, ii) memory, I/O, and communication bandwidth; iii) software; iv) architecture; and v) applications. The processing, memory, and I/O bandwidth in most conventional 35 HPC environments are normally not well balanced and, therefore, do not scale well. Many HPC environments do not have the I/O bandwidth to satisfy high-end data processing requirements or are built with blades that have too many unneeded components installed, which tend to dramatically 40 reduce the system's reliability. Accordingly, many HPC environments may not provide robust cluster management software for efficient operation in production-oriented environments.

SUMMARY OF THE INVENTION

This disclosure provides a system and method for job management in an HPC environment that includes determining an unallocated subset from a plurality of HPC nodes, with each 50 of the unallocated HPC nodes comprising an integrated fabric. An HPC job is selected from a job queue and executed using at least a portion of the unallocated subset of nodes.

The invention has several important technical advantages. For example, one possible advantage of the present invention 55 is that by at least partially reducing, distributing, or eliminating centralized switching functionality, it may provide greater input/output (I/O) performance, perhaps four to eight times the conventional HPC bandwidth. Indeed, in certain embodiments, the I/O performance may nearly equal processor performance. This well-balanced approach may be less sensitive to communications overhead. Accordingly, the present invention may increase blade and overall system performance. A further possible advantage is reduced interconnect latency. Further, the present invention may be more easily scaleable, 65 reliable, and fault tolerant than conventional blades. Yet another advantage may be a reduction of the costs involved in

2

manufacturing an HPC server, which may be passed on to universities and engineering labs, and/or the costs involved in performing HPC processing. The invention may further allow for management software that is more robust and efficient based, at least in part, on the balanced architecture. Various embodiments of the invention may have none, some, or all of these advantages. Other technical advantages of the present invention will be readily apparent to one skilled in the art.

BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present disclosure and its advantages, reference is now made to the following descriptions, taken in conjunction with the accompanying drawings, in which:

FIG.1 illustrates an example high-performance computing system in accordance with one embodiment of the present disclosure:

FIGS. **2**A-D illustrate various embodiments of the grid in the system of FIG. **1** and the usage thereof;

FIGS. 3A-C illustrate various embodiments of individual nodes in the system of FIGS. 1;

FIGS. **4**A-B illustrate various embodiments of a graphical user interface in accordance with the system of FIG. **1**;

FIG. 5 illustrates one embodiment of the cluster management software in accordance with the system in FIG. 1;

FIG. 6 is a flowchart illustrating a method for submitting a batch job in accordance with the high-performance computing system of FIG. 1;

FIG. 7 is a flowchart illustrating a method for dynamic backfilling of the grid in accordance with the high-performance computing system of FIG. 1; and

FIG. 8 is a flow chart illustrating a method for dynamically managing a node failure in accordance with the high-performance computing system of FIG. 1.

DETAILED DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram illustrating a high Performance Computing (HPC) system 100 for executing software applications and processes, for example an atmospheric, weather, or crash simulation, using HPC techniques. System 100 provides users with HPC functionality dynamically allocated among various computing nodes 115 with I/O performance substantially similar to the processing performance. Generally, these nodes 115 are easily scaleable because of, among other things, this increased input/output (I/O) performance and reduced fabric latency. For example, the scalability of nodes 115 in a distributed architecture may be represented by a derivative of Amdahl's law:

S(N)=1/((FP/N)+FS)*(1-Fc*(1-RR/L))

where S(N)=Speedup on N processors, Fp=Fraction of Parallel Code, Fs=Fraction of Non-Parallel Code, Fc=Fraction of processing devoted to communications, and RR/L=Ratio of Remote/Local Memory Bandwidth. Therefore, by HPC system 100 providing I/O performance substantially equal to or nearing processing performance, HPC system 100 increases overall efficiency of HPC applications and allows for easier system administration.

HPC system 100 is a distributed client/server system that allows users (such as scientists and engineers) to submit jobs 150 for processing on an HPC server 102. For example, system 100 may include HPC server 102 that is connected, through network 106, to one or more administration workstations or local clients 120. But system 100 may be a standalone computing environment or any other suitable environment. In

short, system 100 is any HPC computing environment that includes highly scaleable nodes 115 and allows the user to submit jobs 150, dynamically allocates scaleable nodes 115 for job 150, and automatically executes job 150 using the allocated nodes 115. Job 150 may be any batch or online job 5 operable to be processed using HPC techniques and submitted by any apt user. For example, job 150 may be a request for a simulation, a model, or for any other high-performance requirement. Job 150 may also be a request to run a data center application, such as a clustered database, an online transaction processing system, or a clustered application server. The term "dynamically," as used herein, generally means that certain processing is determined, at least in part, at run-time based on one or more variables. The term "automatically," as used herein, generally means that the appropriate 15 processing is substantially performed by at least part of HPC system 100. It should be understood that "automatically" further contemplates any suitable user or administrator interaction with system 100 without departing from the scope of this disclosure.

HPC server 102 comprises any local or remote computer operable to process job 150 using a plurality of balanced nodes 115 and cluster management engine 130. Generally, HPC server 102 comprises a distributed computer such as a blade server or other distributed server. However the configu- 25 ration, server 102 includes a plurality of nodes 115. Nodes 115 comprise any computer or processing device such as, for example, blades, general-purpose personal computers (PC), Macintoshes, workstations, Unix-based computers, or any other suitable devices. Generally, FIG. 1 provides merely one 30 example of computers that may be used with the disclosure. For example, although FIG. 1 illustrates one server 102 that may be used with the disclosure, system 100 can be implemented using computers other than servers, as well as a server pool. In other words, the present disclosure contemplates 35 computers other than general purpose computers as well as computers without conventional operating systems. As used in this document, the term "computer" is intended to encompass a personal computer, workstation, network computer, or any other suitable processing device. HPC server 102, or the 40 component nodes 115, may be adapted to execute any operating system including Linux, UNIX, Windows Server, or any other suitable operating system. According to one embodiment, HPC server 102 may also include or be communicably coupled with a remote web server. Therefore, server 102 may 45 comprise any computer with software and/or hardware in any combination suitable to dynamically allocate nodes 115 to process HPC job 150.

At a high level, HPC server 102 includes a management node 105, a grid 110 comprising a plurality of nodes 115, and cluster management engine 130. More specifically, server 102 may be a standard 19" rack including a plurality of blades (nodes 115) with some or all of the following components: i) dual-processors; ii) large, high bandwidth memory; iii) dual host channel adapters (HCAs); iv) integrated fabric switching; v) FPGA support; and vi) redundant power inputs or N+1 power supplies. These various components allow for failures to be confined to the node level. But it will be understood that HPC server 102 and nodes 115 may not include all of these components.

Management node 105 comprises at least one blade substantially dedicated to managing or assisting an administrator. For example, management node 105 may comprise two blades, with one of the two blades being redundant (such as an active/passive configuration). In one embodiment, management node 105 may be the same type of blade or computing device as HPC nodes 115. But, management node 105 may be

4

any node, including any number of circuits and configured in any suitable fashion, so long as it remains operable to at least partially manage grid 110. Often, management node 105 is physically or logically separated from the plurality of HPC nodes 115, jointly represented in grid 110. In the illustrated embodiment, management node 105 may be communicably coupled to grid 110 via link 108. Link 108 may comprise any communication conduit implementing any appropriate communications protocol. In one embodiment, link 108 provides Gigabit or 10 Gigabit Ethernet communications between management node 105 and grid 110.

Grid 110 is a group of nodes 115 interconnected for increased processing power. Typically, grid 110 is a 3D Torus, but it may be a mesh, a hypercube, or any other shape or configuration without departing from the scope of this disclosure. The links between nodes 115 in grid 110 may be serial or parallel analog links, digital links, or any other type of link that can convey electrical or electromagnetic signals such as, for example, fiber or copper. Each node 115 is configured with an integrated switch. This allows node 115 to more easily be the basic construct for the 3D Torus and helps minimize XYZ distances between other nodes 115. Further, this may make copper wiring work in larger systems at up to Gigabit rates with, in some embodiments, the longest cable being less than 5 meters. In short, node 115 is generally optimized for nearest-neighbor communications and increased I/O bandwidth.

Each node 115 may include a cluster agent 132 communicably coupled with cluster management engine 130. Generally, agent 132 receives requests or commands from management node 105 and/or cluster management engine 130. Agent 132 could include any hardware, software, firmware, or combination thereof operable to determine the physical status of node 115 and communicate the processed data, such as through a "heartbeat," to management node 105. In another embodiment, management node 105 may periodically poll agent 132 to determine the status of the associated node 115. Agent 132 may be written in any appropriate computer language such as, for example, C, C++, Assembler, Java, Visual Basic, and others or any combination thereof so long as it remains compatible with at least a portion of cluster management engine 130.

Cluster management engine 130 could include any hardware, software, firmware, or combination thereof operable to dynamically allocate and manage nodes 115 and execute job 150 using nodes 115. For example, cluster management engine 130 may be written or described in any appropriate computer language including C, C++, Java, Visual Basic, assembler, any suitable version of 4 GL, and others or any combination thereof. It will be understood that while cluster management engine 130 is illustrated in FIG. 1 as a single multi-tasked module, the features and functionality performed by this engine may be performed by multiple modules such as, for example, a physical layer module, a virtual layer module, a job scheduler, and a presentation engine (as shown in more detail in FIG. 5). Further, while illustrated as external to management node 105, management node 105 typically executes one or more processes associated with cluster management engine 130 and may store cluster management 60 engine 130. Moreover, cluster management engine 130 may be a child or sub-module of another software module without departing from the scope of this disclosure. Therefore, cluster management engine 130 comprises one or more software modules operable to intelligently manage nodes 115 and jobs

Server 102 may include interface 104 for communicating with other computer systems, such as client 120, over net-

work 106 in a client-server or other distributed environment. In certain embodiments, server 102 receives jobs 150 or job policies from network 106 for storage in disk farm 140. Disk farm 140 may also be attached directly to the computational array using the same wideband interfaces that interconnects the nodes. Generally, interface 104 comprises logic encoded in software and/or hardware in a suitable combination and operable to communicate with network 106. More specifically, interface 104 may comprise software supporting one or more communications protocols associated with communications network 106 or hardware operable to communicate physical signals.

Network 106 facilitates wireless or wireline communication between computer server 102 and any other computer, such as clients 120. Indeed, while illustrated as residing 15 between server 102 and client 120, network 106 may also reside between various nodes 115 without departing from the scope of the disclosure. In other words, network 106 encompasses any network, networks, or sub-network operable to facilitate communications between various computing com- 20 ponents. Network 106 may communicate, for example, Internet Protocol (IP) packets, Frame Relay frames, Asynchronous Transfer Mode (ATM) cells, voice, video, data, and other suitable information between network addresses. Network 106 may include one or more local area networks 25 (LANs), radio access networks (RANs), metropolitan area networks (MANs), wide area networks (WANs), all or a portion of the global computer network known as the Internet, and/or any other communication system or systems at one or more locations.

In general, disk farm 140 is any memory, database or storage area network (SAN) for storing jobs 150, profiles, boot images, or other HPC information. According to the illustrated embodiment, disk farm 140 includes one or more storage clients 142. Disk farm 140 may process and route data 35 packets according to any of a number of communication protocols, for example, InfiniBand (IB), Gigabit Ethernet (GE), or FibreChannel (FC). Data packets are typically used to transport data within disk farm 140. A data packet may include a header that has a source identifier and a destination 40 identifier. The source identifier, for example, a source address, identifies the transmitter of information, and the destination identifier, for example, a destination address, identifies the recipient of the information.

Client 120 is any device operable to present the user with a 45 job submission screen or administration via a graphical user interface (GUI) 126. At a high level, illustrated client 120 includes at least GUI 126 and comprises an electronic computing device operable to receive, transmit, process and store any appropriate data associated with system 100. It will be 50 understood that there may be any number of clients 120 communicably coupled to server 102. Further, "client 120" and "user of client 120" may be used interchangeably as appropriate without departing from the scope of this disclosure. Moreover, for ease of illustration, each client is 55 described in terms of being used by one user. But this disclosure contemplates that many users may use one computer to communicate jobs 150 using the same GUI 126.

As used in this disclosure, client 120 is intended to encompass a personal computer, touch screen terminal, workstation, 60 network computer, kiosk, wireless data port, cell phone, personal data assistant (PDA), one or more processors within these or other devices, or any other suitable processing device. For example, client 120 may comprise a computer that includes an input device, such as a keypad, touch screen, 65 mouse, or other device that can accept information, and an output device that conveys information associated with the

6

operation of server 102 or clients 120, including digital data, visual information, or GUI 126. Both the input device and output device may include fixed or removable storage media such as a magnetic computer disk, CD-ROM, or other suitable media to both receive input from and provide output to users of clients 120 through the administration and job submission display, namely GUI 126.

GUI 126 comprises a graphical user interface operable to allow i) the user of client 120 to interface with system 100 to submit one or more jobs 150; and/or ii) the system (or network) administrator using client 120 to interface with system 100 for any suitable supervisory purpose. Generally, GUI 126 provides the user of client 120 with an efficient and userfriendly presentation of data provided by HPC system 100. GUI 126 may comprise a plurality of customizable frames or views having interactive fields, pull-down lists, and buttons operated by the user. In one embodiment, GUI 126 presents a job submission display that presents the various job parameter fields and receives commands from the user of client 120 via one of the input devices. GUI 126 may, alternatively or in combination, present the physical and logical status of nodes 115 to the system administrator, as illustrated in FIGS. 4A-B, and receive various commands from the administrator. Administrator commands may include marking nodes as (un) available, shutting down nodes for maintenance, rebooting nodes, or any other suitable command. Moreover, it should be understood that the term graphical user interface may be used in the singular or in the plural to describe one or more graphical user interfaces and each of the displays of a particular graphical user interface. Therefore, GUI 126 contemplates any graphical user interface, such as a generic web browser, that processes information in system 100 and efficiently presents the results to the user. Server 102 can accept data from client 120 via the web browser (e.g., Microsoft Internet Explorer or Netscape Navigator) and return the appropriate HTML or XML responses using network **106**.

In one aspect of operation, HPC server 102 is first initialized or booted. During this process, cluster management engine 130 determines the existence, state, location, and/or other characteristics of nodes 115 in grid 110. As described above, this may be based on a "heartbeat" communicated upon each node's initialization or upon near immediate polling by management node 105. Next, cluster management engine 130 may dynamically allocate various portions of grid 110 to one or more virtual clusters 220 based on, for example, predetermined policies. In one embodiment, cluster management engine 130 continuously monitors nodes 115 for possible failure and, upon determining that one of the nodes 115 failed, effectively managing the failure using any of a variety of recovery techniques. Cluster management engine 130 may also manage and provide a unique execution environment for each allocated node of virtual cluster 220. The execution environment may consist of the hostname, IP address, operating system, configured services, local and shared file systems, and a set of installed applications and data. The cluster management engine 130 may dynamically add or subtract nodes from virtual cluster 220 according to associated policies and according to inter-cluster policies, such as priority.

When a user logs on to client 120, he may be presented with a job submission screen via GUI 126. Once the user has entered the job parameters and submitted job 150, cluster management engine 130 processes the job submission, the related parameters, and any predetermined policies associated with job 150, the user, or the user group. Cluster management engine 130 then determines the appropriate virtual cluster 220 based, at least in part, on this information. Engine 130 then dynamically allocates a job space 230 within virtual

cluster 220 and executes job 150 across the allocated nodes 115 using HPC techniques. Based, at least in part, on the increased I/O performance, HPC server 102 may more quickly complete processing of job 150. Upon completion, cluster management engine communicates results 160 to the 5 user.

FIGS. 2A-D illustrate various embodiments of grid 210 in system 100 and the usage or topology thereof. FIG. 2A illustrates one configuration, namely a 3D Torus, of grid 210 using a plurality of node types. For example, the illustrated node 10 types are external I/O node, FS server, FS metadata server, database server, and compute node. FIG. 2B illustrates an example of "folding" of grid 210. Folding generally allows for one physical edge of grid 215 to connect to a corresponding axial edge, thereby providing a more robust or edgeless 15 topology. In this embodiment, nodes 215 are wrapped around to provide a near seamless topology connect by node link 216. Node line 216 may be any suitable hardware implementing any communications protocol for interconnecting two or more nodes 215. For example, node line 216 may be copper 20 wire or fiber optic cable implementing Gigabit Ethernet.

FIG. 2C illustrates grid 210 with one virtual cluster 220 allocated within it. While illustrated with only one virtual cluster 220, there may be any number (including zero) of virtual clusters 220 in grid 210 without departing from the 25 scope of this disclosure. Virtual cluster 220 is a logical grouping of nodes 215 for processing related jobs 150. For example, virtual cluster 220 may be associated with one research group, a department, a lab, or any other group of users likely to submit similar jobs 150. Virtual cluster 220 may be any shape and include any number of nodes 215 within grid 210. Indeed, while illustrated virtual cluster 220 includes a plurality of physically neighboring nodes 215, cluster 220 may be a distributed cluster of logically related nodes 215 operable to process job 150.

Virtual cluster 220 may be allocated at any appropriate time. For example, cluster 220 may be allocated upon initialization of system 100 based, for example, on startup parameters or may be dynamically allocated based, for example, on changed server 102 needs. Moreover, virtual cluster 220 may 40 change its shape and size over time to quickly respond to changing requests, demands, and situations. For example; virtual cluster 220 may be dynamically changed to include an automatically allocated first node 215 in response to a failure of a second node 215, previously part of cluster 220. In certain 45 embodiments, clusters 220 may share nodes 215 as processing requires.

FIG. 2D illustrates various job spaces, 230a and 230b respectively, allocated within example virtual cluster 220. Generally, job space 230 is a set of nodes 215 within virtual 50 cluster 220 dynamically allocated to complete received job 150. Typically, there is one job space 230 per executing job 150 and vice versa, but job spaces 230 may share nodes 215 without departing from the scope of the disclosure. The dimensions of job space 230 may be manually input by the 55 user or administrator or dynamically determined based on job parameters, policies, and/or any other suitable characteristic.

FIGS. 3A-C illustrate various embodiments of individual nodes 115 in grid 110. In the illustrated, but example, embodiments, nodes 115 are represented by blades 315. Blade 315 comprises any computing device in any orientation operable to process all or a portion, such as a thread or process, of job 150. For example, blade 315 may be a standard Xeon64TM motherboard, a standard PCI-Express OpteronTM motherboard, or any other suitable computing card.

Blade 315 is an integrated fabric architecture that distributes the fabric switching components uniformly across nodes

8

115 in grid 110, thereby possibly reducing or eliminating any centralized switching function, increasing the fault tolerance, and allowing message passing in parallel. More specifically, blade 315 includes an integrated switch 345. Switch 345 includes any number of ports that may allow for different topologies. For example, switch 345 may be an eight-port switch that enables a tighter three-dimensional mesh or 3D Torus topology. These eight ports include two "X" connections for linking to neighbor nodes 115 along an X-axis, two "Y" connections for linking to neighbor nodes 115 along a Y-axis, two "Z" connections for linking to neighbor nodes 115 along a Z-axis, and two connections for linking to management node 105. In one embodiment, switch 345 may be a standard eight port Infiniband-4x switch IC, thereby easily providing built-in fabric switching. Switch 345 may also comprise a twenty-four port switch that allows for multidimensional topologies, such a 4-D Torus, or other non-traditional topologies of greater than three dimensions. Moreover, nodes 115 may further interconnected along a diagonal axis, thereby reducing jumps or hops of communications between relatively distant nodes 115. For example, a first node 115 may be connected with a second node 115 that physically resides along a northeasterly axis several three dimensional "jumps" away.

FIG. 3A illustrates a blade 315 that, at a high level, includes at least two processors 320a and 320b, local or remote memory 340, and integrated switch (or fabric) 345. Processor 320 executes instructions and manipulates data to perform the operations of blade 315 such as, for example, a central processing unit (CPU). Reference to processor 320 is meant to include multiple processors 320 where applicable. In one embodiment, processor 320 may comprise a Xeon64 or Itanium™ processor or other similar processor may be a 3.4 GHz chip with a 2 MB Cache and HyperTreading. In this embodiment, the dual processor module may include a native PCI/Express that improves efficiency. Accordingly, processor 320 has efficient memory bandwidth and, typically, has the memory controller built into the processor chip.

Blade 315 may also include Northbridge 321, Southbridge 322, PCI channel 325, HCA 335, and memory 340. Northbridge 321 communicates with processor 320 and controls communications with memory 340, a PCI bus, Level 2 cache, and any other related components. In one embodiment, Northbridge 321 communicates with processor 320 using the frontside bus (FSB). Southbridge 322 manages many of the input/output (I/O) functions of blade 315. In another embodiment, blade 315 may implement the Intel Hub Architecture (IHATM), which includes a Graphics and AGP Memory Controller Hub (GMCH) and an I/O Controller Hub (ICH).

PCI channel 325 comprises any high-speed, low latency link designed to increase the communication speed between integrated components. This helps reduce the number of buses in blade 315, which can reduce system bottlenecks. HCA 335 comprises any component providing channel-based I/O within server 102. Each HCA 335 may provide a total bandwidth of 2.65 GB/sec, thereby allowing 1.85 GB/sec per PE to switch 345 and 800 MB/sec per PE to I/O such as, for example, BIOS (Basic Input/Output System), an Ethernet management interface, and others. This further allows the total switch 345 bandwidth to be 3.7 GB/sec for 13.6 Gigaflops/sec peak or 0.27 Bytes/Flop I/O rate is 50 MB/sec per Gigaflop.

Memory 340 includes any memory or database module and may take the form of volatile or non-volatile memory including, without limitation, magnetic media, optical media, flash memory, random access memory (RAM), read-only memory

(ROM), removable media, or any other suitable local or remote memory component. In the illustrated embodiment, memory **340** is comprised of 8 GB of dual double data rate (DDR) memory components operating at least 6.4 GB/s. Memory **340** may include any appropriate data for managing or executing HPC jobs **150** without departing from this disclosure.

FIG. 3B illustrates a blade **315** that includes two processors **320***a* and **320***b*, memory **340**, HyperTransport/peripheral component interconnect (HT/PCI) bridges **330***a* and **330***b*, 10 and two HCAs **335***a* and **335***b*.

Example blade 315 includes at least two processors 320. Processor 320 executes instructions and manipulates data to perform the operations of blade 315 such as, for example, a central processing unit (CPU). In the illustrated embodiment, processor 320 may comprise an Opteron processor or other similar processor or derivative. In this embodiment, the Opteron processor design supports the development of a well balanced building block for grid 110. Regardless, the dual processor module may provide four to five Gigaflop usable 20 performance and the next generation technology helps solve memory bandwidth limitation. But blade 315 may more than two processors 320 without departing from the scope of this disclosure. Accordingly, processor 320 has efficient memory bandwidth and, typically, has the memory controller built into 25 the processor chip. In this embodiment, each processor 320 has one or more HyperTransportTM (or other similar conduit type) links 325.

Generally, HT link **325** comprises any high-speed, low latency link designed to increase the communication speed 30 between integrated components. This helps reduce the number of buses in blade **315**, which can reduce system bottlenecks. HT link **325** supports processor to processor communications for cache coherent multiprocessor blades **315**. Using HT links **325**, up to eight processors **320** may be placed 315 on blade **315**. If utilized, HyperTransport may provide bandwidth of 6.4 GB/sec, 12.8, or more, thereby providing a better than forty-fold increase in data throughput over legacy PCI buses. Further HyperTransport technology may be compatible with legacy I/O standards, such as PCI, and other technologies, such as PCI-X.

Blade 315 further includes HT/PCI bridge 330 and HCA 335. PCI bridge 330 may be designed in compliance with PCI Local Bus Specification Revision 2.2 or 3.0 or PCI Express Base Specification 1.0a or any derivatives thereof. HCA 335 45 comprises any component providing channel-based I/O within server 102. In one embodiment, HCA 335 comprises an Infiniband HCA. InfiniBand channels are typically created by attaching host channel adapters and target channel adapters, which enable remote storage and network connectivity 50 into an InfiniBand fabric, illustrated in more detail in FIG. 3B. Hypertransport 325 to PCI-Express Bridge 330 and HCA 335 may create a full-duplex 2 GB/sec I/O channel for each processor 320. In certain embodiments, this provides sufficient bandwidth to support processor-processor communications 55 in distributed HPC environment 100. Further, this provides blade 315 with I/O performance nearly or substantially balanced with the performance of processors 320.

FIG. 3C illustrates another embodiment of blade 315 including a daughter board. In this embodiment, the daughter 60 board may support 3.2 GB/sec or higher cache coherent interfaces. The daughter board is operable to include one or more Field Programmable Gate Arrays (FPGAs) 350. For example, the illustrated daughter board includes two FPGAs 350, represented by 350a and 350b, respectively. Generally, FPGA 65 350 provides blade 315 with non-standard interfaces, the ability to process custom algorithms, vector processors for

10

signal, image, or encryption/decryption processing applications, and high bandwidth. For example, FPGA may supplement the ability of blade 315 by providing acceleration factors of ten to twenty times the performance of a general purpose processor for special functions such as, for example, low precision Fast Fourier Transform (FFT) and matrix arithmetic functions.

The preceding illustrations and accompanying descriptions provide exemplary diagrams for implementing various scaleable nodes 115 (illustrated as example blades 315). However, these figures are merely illustrative and system 100 contemplates using any suitable combination and arrangement of elements for implementing various scalability schemes. Although the present invention has been illustrated and described, in part, in regard to blade server 102, those of ordinary skill in the art will recognize that the teachings of the present invention may be applied to any clustered HPC server environment. Accordingly, such clustered servers 102 that incorporate the techniques described herein may be local or a distributed without departing from the scope of this disclosure. Thus, these servers 102 may include HPC modules (or nodes 115) incorporating any suitable combination and arrangement of elements for providing high performance computing power, while reducing I/O latency. Moreover, the operations of the various illustrated HPC modules may be combined and/or separated as appropriate. For example, grid 110 may include a plurality of substantially similar nodes 115 or various nodes 115 implementing differing hardware or fabric architecture.

FIGS. 4A-B illustrate various embodiments of a management graphical user interface 400 in accordance with the system 100. Often, management GUI 400 is presented to client 120 using GUI 126. In general, management GUI 400 presents a variety of management interactive screens or displays to a system administrator and/or a variety of job submission or profile screens to a user. These screens or displays are comprised of graphical elements assembled into various views of collected information. For example, GUI 400 may present a display of the physical health of grid 110 (illustrated in FIG. 4A) or the logical allocation or topology of nodes 115 in grid 110 (illustrated in FIG. 4B).

FIG. 4A illustrates example display 400a. Display 400a may include information presented to the administrator for effectively managing nodes 115. The illustrated embodiment includes a standard web browser with a logical "picture" or screenshot of grid 110. For example, this picture may provide the physical status of grid 110 and the component nodes 115. Each node 115 may be one of any number of colors, with each color representing various states. For example, a failed node 115 may be red, a utilized or allocated node 115 may be black, and an unallocated node 115 may be shaded. Further, display 400a may allow the administrator to move the pointer over one of the nodes 115 and view the various physical attributes of it. For example, the administrator may be presented with information including "node," "availability," "processor utilization," "memory utilization," "temperature," "physical location," and "address." Of course, these are merely example data fields and any appropriate physical or logical node information may be display for the administrator. Display 400a may also allow the administrator to rotate the view of grid 110 or perform any other suitable function.

FIG. 4B illustrates example display 400b. Display 400b presents a view or picture of the logical state of grid 100. The illustrated embodiment presents the virtual cluster 220 allocated within grid 110. Display 400b further displays two example job spaces 230 allocate within cluster 220 for executing one or more jobs 150. Display 400b may allow the

administrator to move the pointer over graphical virtual cluster **220** to view the number of nodes **115** grouped by various statuses (such as allocated or unallocated). Further, the administrator may move the pointer over one of the job spaces **230** such that suitable job information is presented. For example, the administrator may be able to view the job name, start time, number of nodes, estimated end time, processor usage, I/O usage, and others.

It will be understood that management GUI 126 (represented above by example displays 400a and 400b, respectively) is for illustration purposes only and may include none, some, or all of the illustrated graphical elements as well as additional management elements not shown.

FIG. 5 illustrates one embodiment of cluster management engine 130, shown here as engine 500, in accordance with system 100. In this embodiment, cluster management engine 500 includes a plurality of sub-modules or components: physical manager 505, virtual manager 510, job scheduler 515, and local memory or variables 520.

Physical manager **505** is any software, logic, firmware, or other module operable to determine the physical health of various nodes **115** and effectively manage nodes **115** based on this determined health. Physical manager may use this data to efficiently determine and respond to node **115** failures. In one 25 embodiment, physical manager **505** is communicably coupled to a plurality of agents **132**, each residing on one node **115**. As described above, agents **132** gather and communicate at least physical information to manager **505**. Physical manager **505** may be further operable to communicate 30 alerts to a system administrator at client **120** via network **106**.

Virtual manager 510 is any software, logic, firmware, or other module operable to manage virtual clusters 220 and the logical state of nodes 115. Generally, virtual manager 510 links a logical representation of node 115 with the physical 35 status of node 115. Based on these links, virtual manager 510 may generate virtual clusters 220 and process various changes to these clusters 220, such as in response to node failure or a (system or user) request for increased HPC processing. Virtual manager 510 may also communicate the sta- 40 tus of virtual cluster 220, such as unallocated nodes 115, to job scheduler 515 to enable dynamic backfilling of unexecuted, or queued, HPC processes and jobs 150. Virtual manager 510 may further determine the compatibility of job 150 with particular nodes 115 and communicate this information 45 to job scheduler 515. In certain embodiments, virtual manager 510 may be an object representing an individual virtual cluster 220.

Cluster management engine 500 may also include job scheduler 515. Job scheduler sub-module 515 is a topology- 50 aware module that processes aspects of the system's resources, as well with the processors and the time allocations, to determine an optimum job space 230 and time. Factors that are often considered include processors, processes, memory, interconnects, disks, visualization engines, 55 and others. In other words, job scheduler 515 typically interacts with GUI 126 to receive jobs 150, physical manager 505 to ensure the health of various nodes 115, and virtual manager 510 to dynamically allocate job space 230 within a certain virtual cluster 220. This dynamic allocation is accomplished 60 through various algorithms that often incorporates knowledge of the current topology of grid 110 and, when appropriate, virtual cluster 220. Job scheduler 515 handles both batch and interactive execution of both serial and parallel programs. Scheduler 515 should also provide a way to implement poli- 65 cies 524 on selecting and executing various problems presented by job 150.

12

Cluster management engine **500**, such as through job scheduler **515**, may be further operable to perform efficient check-pointing. Restart dumps typically comprise over seventy-five percent of data written to disk. This I/O is often done so that processing is not lost to a platform failure. Based on this, a file system's I/O can be segregated into two portions: productive I/O and defensive I/O. Productive I/O is the writing of data that the user calls for to do science such as, for example, visualization dumps, traces of key physics variables over time, and others. Defensive I/O is performed to manage a large simulation run over a substantial period of time. Accordingly, increased I/O bandwidth greatly reduces the time and risk involved in check-pointing.

Returning to engine 500, local memory 520 comprises logical descriptions (or data structures) of a plurality of features of system 100. Local memory 520 may be stored in any physical or logical data storage operable to be defined, processed, or retrieved by compatible code. For example, local memory 520 may comprise one or more extensible Markup 20 Language (XML) tables or documents. The various elements may be described in terms of SQL statements or scripts, Virtual Storage Access Method (VSAM) files, flat files, binary data files, Btrieve files, database files, or commaseparated-value (CSV) files. It will be understood that each element may comprise a variable, table, or any other suitable data structure. Local memory 520 may also comprise a plurality of tables or files stored on one server 102 or across a plurality of servers or nodes. Moreover, while illustrated as residing inside engine 500, some or all of local memory 520 may be internal or external without departing from the scope of this disclosure.

Illustrated local memory 520 includes physical list 521, virtual list 522, group file 523, policy table 524, and job queue 525. But, while not illustrated, local memory 520 may include other data structures, including a job table and audit log, without departing from the scope of this disclosure. Returning to the illustrated structures, physical list 521 is operable to store identifying and physical management information about node 115. Physical list 521 may be a multi-dimensional data structure that includes at least one record per node 115. For example, the physical record may include fields such as "node," "availability," "processor utilization," "memory utilization," "temperature," "physical location," "address," "boot images," and others. It will be understood that each record may include none, some, or all of the example fields. In one embodiment, the physical record may provide a foreign key to another table, such as, for example, virtual list 522.

Virtual list 522 is operable to store logical or virtual management information about node 115. Virtual list 522 may be a multi-dimensional data structure that includes at least one record per node 115. For example, the virtual record may include fields such as "node," "availability," "job," "virtual cluster," "secondary node," "logical location," "compatibility," and others. It will be understood that each record may include none, some, or all of the example fields. In one embodiment, the virtual record may include a link to another table such as, for example, group file 523.

Group file **523** comprises one or more tables or records operable to store user group and security information, such as access control lists (or ACLs). For example, each group record may include a list of available services, nodes **115**, or jobs for a user. Each logical group may be associated with a business group or unit, a department, a project, a security group, or any other collection of one or more users that are able to submit jobs **150** or administer at least part of system **100**. Based on this information, cluster management engine **500** may determine if the user submitting job **150** is a valid

user and, if so, the optimum parameters for job execution. Further, group table 523 may associate each user group with a virtual cluster 220 or with one or more physical nodes 115, such as nodes residing within a particular group's domain. This allows each group to have an individual processing space 5 without competing for resources. However, as described above, the shape and size of virtual cluster 220 may be dynamic and may change according to needs, time, or any other parameter.

Policy table **524** includes one or more policies. It will be 10 understood that policy table 524 and policy 524 may be used interchangeably as appropriate. Policy 524 generally stores processing and management information about jobs 150 and/ or virtual clusters 220. For example, policies 524 may include any number of parameters or variables including problem 15 size, problem run time, timeslots, preemption, users' allocated share of node 115 or virtual cluster 220, and such.

Job queue 525 represents one or more streams of jobs 150 awaiting execution. Generally, queue 525 comprises any suitable data structure, such as a bubble array, database table, or 20 pointer array, for storing any number (including zero) of jobs 150 or reference thereto. There may be one queue 525 associated with grid 110 or a plurality of queues 525, with each queue 525 associated with one of the unique virtual clusters **220** within grid **110**.

In one aspect of operation, cluster management engine 500 receives job 150, made up of N tasks which cooperatively solve a problem by performing calculations and exchanging information. Cluster management engine 500 allocates N nodes 115 and assigns each of the N tasks to one particular 30 node 515 using any suitable technique, thereby allowing the problem to be solved efficiently. For example, cluster management engine 500 may utilize job parameters, such as job task placement strategy, supplied by the user. Regardless, cluster management engine 500 attempts to exploit the archi-35 tecture of server 102, which in turn provides the quicker turnaround for the user and likely improves the overall throughput for system 100.

In one embodiment, cluster management engine 500 then lowing example topologies:

Specified 2D (x,y) or 3D (x,y,z)—Nodes 115 are allocated and tasks may be ordered in the specified dimensions, thereby preserving efficient neighbor to neighbor communication. The specified topology manages a variety of jobs 150 where 45 it is desirable that the physical communication topology match the problem topology allowing the cooperating tasks of job 150 to communicate frequently with neighbor tasks. For example, a request of 8 tasks in a 2.times.2.times.2 dimension (2, 2, 2) will be allocated in a cube. For best-fit purposes, 2D 50 allocations can be "folded" into 3 dimensions (as discussed in FIG. 2D), while preserving efficient neighbor to neighbor communications. Cluster management engine 500 may be free to allocate the specified dimensional shape in any orientation. For example, a 2.times.2.times.8 box may be allocated 55 within the available physical nodes vertically or horizontally

Best Fit Cube—cluster management engine 500 allocates N nodes 115 in a cubic volume. This topology efficiently handles jobs 150 allowing cooperating tasks to exchange data with any other tasks by minimizing the distance between any 60 two nodes 115.

Best Fit Sphere—cluster management engine 500 allocates N nodes 115 in a spherical volume. For example, the first task may be placed in the center node 115 of the sphere with the rest of the tasks placed on nodes 115 surrounding the 65 center node 115. It will be understood that the placement order of the remaining tasks is not typically critical. This

14

topology may minimize the distance between the first task and all other tasks. This efficiently handles a large class of problems where tasks 2-N communicate with the first task, but not with each other.

Random—cluster management engine 500 allocates N nodes 115 with reduced consideration for where nodes 115 are logically or physically located. In one embodiment, this topology encourages aggressive use of grid 110 for backfilling purposes, with little impact to other jobs 150.

It will be understood that the prior topologies and accompanying description are for illustration purposes only and may not depict actual topologies used or techniques for allocating such topologies.

Cluster management engine 500 may utilize a placement weight, stored as a job 150 parameter or policy 524 parameter. In one embodiment, the placement weight is a modifier value between 0 and 1, which represents how aggressively cluster management engine 500 should attempt to place nodes 115 according to the requested task (or process) placement strategy. In this example, a value of 0 represents placing nodes 115 only if the optimum strategy (or dimensions) is possible and a value of 1 represents placing nodes 115 immediately, as long as there are enough free or otherwise available nodes 115 to handle the request. Typically, the placement weight does 25 not override administrative policies 524 such as resource reservation, in order to prevent starvation of large jobs 150 and preserve the job throughput of HPC system 100.

The preceding illustration and accompanying description provide an exemplary modular diagram for engine 500 implementing logical schemes for managing nodes 115 and jobs **150**. However, this figure is merely illustrative and system 100 contemplates using any suitable combination and arrangement of logical elements for implementing these and other algorithms. Thus, these software modules may include any suitable combination and arrangement of elements for effectively managing nodes 115 and jobs 150. Moreover, the operations of the various illustrated modules may be combined and/or separated as appropriate.

FIG. 6 is a flowchart illustrating an example method 600 selects and allocates nodes 115 according to any of the fol- 40 for dynamically processing a job submission in accordance with one embodiment of the present disclosure. Generally, FIG. 6 describes method 600, which receives a batch job submission, dynamically allocates nodes 115 into a job space 230 based on the job parameters and associated policies 524, and executes job 150 using the allocated space. The following description focuses on the operation of cluster management module 130 in performing method 600. But system 100 contemplates using any appropriate combination and arrangement of logical elements implementing some or all of the described functionality, so long as the functionality remains appropriate.

Method 600 begins at step 605, where HPC server 102 receives job submission 150 from a user. As described above, in one embodiment the user may submit job 150 using client 120. In another embodiment, the user may submit job 150 directly using HPC server 102. Next, at step 610, cluster management engine 130 selects group 523 based upon the user. Once the user is verified, cluster management engine 130 compares the user to the group access control list (ACL) at step 615. But it will be understood that cluster management engine 130 may use any appropriate security technique to verify the user. Based upon determined group 523, cluster management engine 130 determines if the user has access to the requested service. Based on the requested service and hostname, cluster management engine 130 selects virtual cluster 220 at step 620. Typically, virtual cluster 220 may be identified and allocated prior to the submission of job 150.

But, in the event virtual cluster 220 has not been established, cluster management engine 130 may automatically allocate virtual cluster 220 using any of the techniques described above. Next, at step 625, cluster management engine 130 retrieves policy 524 based on the submission of job 150. In 5 one embodiment, cluster management engine 130 may determine the appropriate policy 524 associated with the user, job 150, or any other appropriate criteria. Cluster management engine 130 then determines or otherwise calculates the dimensions of job 150 at step 630. It will be understood that the appropriate dimensions may include length, width, height, or any other appropriate parameter or characteristic. As described above, these dimensions are used to determine the appropriate job space 230 (or subset of nodes 115) within virtual cluster 220. After the initial parameters have been 15 established, cluster management 130 attempts to execute job 150 on HPC server 102 in steps 635 through 665.

At decisional step 635, cluster management engine 130 determines if there are enough available nodes to allocate the desired job space 230, using the parameters already estab- 20 lished. If there are not enough nodes 115, then cluster management engine 130 determines the earliest available subset 230 of nodes 115 in virtual cluster 220 at step 640. Then, cluster management engine 130 adds job 150 to job queue 125 until the subset 230 is available at step 645. Processing then 25 returns to decisional step 635. Once there are enough nodes 115 available, then cluster management engine 130 dynamically determines the optimum subset 230 from available nodes 115 at step 650. It will be understood that the optimum subset 230 may be determined using any appropriate criteria, 30 including fastest processing time, most reliable nodes 115, physical or virtual locations, or first available nodes 115. At step 655, cluster management engine 130 selects the determined subset 230 from the selected virtual cluster 220. Next, at step 660, cluster management engine 130 allocates the 35 selected nodes 115 for job 150 using the selected subset 230. According to one embodiment, cluster management engine 130 may change the status of nodes 115 in virtual node list 522 from "unallocated" to "allocated". Once subset 230 has been appropriately allocated, cluster management engine 130 40 executes job 150 at step 665 using the allocated space based on the job parameters, retrieved policy 524, and any other suitable parameters. At any appropriate time, cluster management engine 130 may communicate or otherwise present job results 160 to the user. For example, results 160 may be 45 formatted and presented to the user via GUI 126.

FIG. 7 is a flowchart illustrating an example method 700 for dynamically backfilling a virtual cluster 220 in grid 110 in accordance with one embodiment of the present disclosure. At a high level, method 700 describes determining available 50 space in virtual cluster 220, determining the optimum job 150 that is compatible with the space, and executing the determined job 150 in the available space. The following description will focus on the operation of cluster management module 130 in performing this method. But, as with the previous 55 flowchart, system 100 contemplates using any appropriate combination and arrangement of logical elements implementing some or all of the described functionality.

Method 700 begins at step 705, where cluster management engine 130 sorts job queue 525. In the illustrated embodiment, cluster management engine 130 sorts the queue 525 based on the priority of jobs 150 stored in the queue 525. But it will be understood that cluster management engine 130 may sort queue 525 using any suitable characteristic such that the appropriate or optimal job 150 will be executed. Next, at 65 step 710, cluster management engine 130 determines the number of available nodes 115 in one of the virtual clusters

16

220. Of course, cluster management engine 130 may also determine the number of available nodes 115 in grid 110 or in any one or more of virtual clusters 220. At step 715, cluster management engine 130 selects first job 150 from sorted job queue 525. Next, cluster management engine 130 dynamically determines the optimum shape (or other dimensions) of selected job 150 at 720. Once the optimum shape or dimension of selected job 150 is determined, then cluster management engine 130 determines if it can backfill job 150 in the appropriate virtual cluster 220 in steps 725 through 745.

At decisional step 725, cluster management engine 130 determines if there are enough nodes 115 available for the selected job 150. If there are enough available nodes 115, then at step 730 cluster management engine 130 dynamically allocates nodes 115 for the selected job 150 using any appropriate technique. For example, cluster management engine 130 may use the techniques describes in FIG. 6. Next, at step 735, cluster management engine 130 recalculates the number of available nodes in virtual cluster 220. At step 740, cluster management engine 130 executes job 150 on allocated nodes 115. Once job 150 has been executed (or if there were not enough nodes 115 for selected job 150), then cluster management engine 130 selects the next job 150 in the sorted job queue 525 at step 745 and processing returns to step 720. It will be understood that while illustrated as a loop, cluster management engine 130 may initiate, execute, and terminate the techniques illustrated in method 700 at any appropriate time.

FIG. 8 is a flowchart illustrating an example method 800 for dynamically managing failure of a node 115 in grid 110 in accordance with one embodiment of the present disclosure. At a high level, method 800 describes determining that node 115 failed, automatically performing job recovery and management, and replacing the failed node 115 with a secondary node 115. The following description will focus on the operation of cluster management module 130 in performing this method. But, as with the previous flowcharts, system 100 contemplates using any appropriate combination and arrangement of logical elements implementing some or all of the described functionality.

Method 800 begins at step 805, where cluster management engine 130 determines that node 115 has failed. As described above, cluster management engine 130 may determine that node 115 has failed using any suitable technique. For example, cluster management engine 130 may pull nodes 115 (or agents 132) at various times and may determine that node 115 has failed based upon the lack of a response from node 115. In another example, agent 132 existing on node 115 may communicate a "heartbeat" and the lack of this "heartbeat" may indicate node 115 failure. Next, at step 810, cluster management engine 130 removes the failed node 115 from virtual cluster 220. In one embodiment, cluster management engine 130 may change the status of node 115 in virtual list 522 from "allocated" to "failed". Cluster management engine 130 then determines if a job 150 is associated with failed node 115 at decisional step 815. If there is no job 150 associated with node 115, then processing ends. As described above, before processing ends, cluster management engine 130 may communicate an error message to an administrator, automatically determine a replacement node 115, or any other suitable processing. If there is a job 150 associated with the failed node 115, then the cluster management engine 130 determines other nodes 115 associated with the job 150 at step 820. Next, at step 825, cluster management engine 130 kills job 150 on all appropriate nodes 115. For example, cluster management engine 130 may execute a kill job command or use any other appropriate technique to end job 150. Next, at step

830, cluster management engine 130 de-allocates nodes 115 using virtual list 522. For example, cluster management engine 130 may change the status of nodes 115 in virtual list 522 from "allocated" to "available". Once the job has been terminated and all appropriate nodes 115 de-allocated, then 5 cluster management engine 130 attempts to re-execute the job 150 using available nodes 115 in steps 835 through 850.

At step 835, cluster management engine 130 retrieves policy 524 and parameters for the killed job 150 at step 835. Cluster management engine 130 then determines the opti- 10 mum subset 230 of nodes 115 in virtual cluster 220, at step 840, based on the retrieved policy 524 and the job parameters. Once the subset 230 of nodes 115 has been determined, then cluster management engine 130 dynamically allocates the subset 230 of nodes 115 at step 845. For example, cluster management engine 130 may change the status of nodes 115 in virtual list 522 from "unallocated" to "allocated". It will be understood that this subset of nodes 115 may be different from the original subset of nodes that job 150 was executing on. For example, cluster management engine 130 may deter- 20 mine that a different subset of nodes is optimal because of the node failure that prompted this execution. In another example, cluster management engine 130 may have determined that a secondary node 115 was operable to replace the failed node 115 and the new subset 230 is substantially similar to the old job space 230. Once the allocated subset 230 has been determined and allocated, then cluster management engine 130 executes job 150 at step 850.

The preceding flowcharts and accompanying description illustrate exemplary methods 600, 700, and 800. In short, 30 system 100 contemplates using any suitable technique for performing these and other tasks. Accordingly, many of the steps in this flowchart may take place simultaneously and/or in different orders than as shown. Moreover, system 100 may use methods with additional steps, fewer steps, and/or different steps, so long as the methods remain appropriate.

Although this disclosure has been described in terms of certain embodiments and generally associated methods, alterations and permutations of these embodiments and methods will be apparent to those skilled in the art. Accordingly, 40 the above description of example embodiments does not define or constrain this disclosure. Other changes, substitutions, and alterations are also possible without departing from the spirit and scope of this disclosure.

What is claimed is:

- 1. A method comprising:
- determining, using one or more computers, an original subset of a plurality of communicatively coupled nodes of a computing environment, the original subset comprising nodes currently unallocated to a job;

selecting a job from a job queue; and

- determining that dimensions of the selected job are greater than a topology of the original subset;
- selecting one or more nodes from a second plurality of nodes, the second plurality being distinct from the original subset, wherein the selected one or more nodes from the second plurality are unavailable at the time of selecting; and
- adding the nodes selected from the second plurality to the original subset to satisfy the dimensions of the selected 60 job after the nodes selected from the second plurality become available; and

executing the selected job.

2. The method of claim 1, further comprising executing the selected job using one or more nodes of the original subset 65 and one or more nodes of the selected second plurality of nodes.

18

- 3. The method of claim 1, further comprising selecting the job from the job queue based on priority.
- **4**. The method of claim **1**, wherein the dimensions of the job are based, at least in part, on one or more job parameters and an associated policy.
 - 5. The method of claim 1, further comprising:
 - dynamically allocating a job space from the original subset and the second plurality of nodes based, at least in part, on the dimensions of the job, wherein executing the selected job comprises executing the selected job using the dynamically allocated job space.
 - 6. The method of claim 1, further comprising:
 - determining that a second job that was executing on a second subset in the plurality of nodes has failed;
 - adding the second subset to the original subset; and adding the failed job to the job queue.
- 7. Software in one or more non-transitory, tangible computer-readable media and when executed operable to:
 - determine an original subset of a plurality of communicatively coupled nodes of a computing environment, the original subset comprising nodes currently unallocated to a job;

select a job from a job queue;

- determine that dimensions of the selected job are greater than a topology of the original subset;
- select one or more nodes from a second plurality of nodes, the second plurality being distinct from the original subset, wherein the selected one or more nodes from the second plurality are unavailable at the time of selecting,
- add the selected second nodes to the original subset to satisfy the dimensions of the selected job after the nodes selected from the second plurality become available; and execute the selected job.
- **8**. The software of claim **7**, wherein the software is operable to select the job from the job queue based on priority.
- 9. The software of claim 7, wherein the dimensions of the job are based, at least in part, on one or more job parameters and an associated policy.
 - 10. The software of claim 7, further operable to:
 - dynamically allocate a job space from the original subset and the selected second nodes based, at least in part, on the dimensions of the job; and
 - wherein the software operable to execute the selected job comprises software operable to execute the selected job using the dynamically allocated job space.
 - 11. A system comprising:

45

- a plurality of communicatively coupled nodes of a computing environment; and
- a management node operable to:
 - determine an original subset of the plurality of nodes, the original subset comprising nodes currently unallocated to a job;
 - select a job from a job queue;
 - determine that dimensions of the selected job are greater than a topology of the original subset;
 - select one or more nodes from a second plurality of nodes, the second plurality being distinct from the original subset, wherein the selected one or more nodes from the second plurality are unavailable at the time of selecting,
 - add the selected one or more nodes from the second plurality to the original subset to satisfy the dimensions of the selected job after the nodes selected from the second plurality become available; and
- execute the selected job using one or more processors of one or more nodes of the modified original subset.

19 20

- 12. The system of claim 11, wherein the management node is operable to select the job from the job queue based on priority.
- 13. The system of claim 12, wherein the dimensions of the job are based, at least in part, on one or more job parameters 5 and an associated policy.
- 14. The system of claim 12, wherein the management node is further operable to:
 - dynamically allocate a job space from the modified original subset of nodes based, at least in part, on the dimensions of the job; and
 - execute the selected job using the dynamically allocated job space.
- **15**. The system of claim **11**, wherein the management node is further operable to return the selected second nodes to the 15 second plurality.
- 16. The system of claim 11, wherein the management node is further operable to:
 - determine that a job that was executing on a second subset in the plurality of nodes has failed;
 - add the second subset to the unallocated subset; and add the failed job to the job queue.

* * * * *

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 9,189,275 B2 Page 1 of 2

APPLICATION NO. : 13/712423

DATED : November 17, 2015 INVENTOR(S) : Davidson et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON THE TITLE PAGE

On page 3, in column 1, under "Other Publications", line 8, delete "11/107,4467;" and insert --11/107,467;--, therefor

On page 3, in column 1, under "Other Publications", line 10, delete "11/107,4467;" and insert --11/107,467;--, therefor

On page 3, in column 1, under "Other Publications", line 10, delete "Mar. 7, 2008." and insert -- Feb. 7, 2008.--, therefor

On page 3, in column 2, under "Other Publications", line 1, delete "Communication," and insert --Communication,--, therefor

On page 3, in column 2, under "Other Publications", line 31, delete "Oct. 15, 200," and insert --Oct. 15, 2009,--, therefor

On page 3, in column 2, under "Other Publications", line 59, delete "Opininion;" and insert --Opinion;--, therefor

On page 4, in column 1, under "Other Publications", line 8, delete "Muticomputer" and insert --Multicomputer--, therefor

On page 4, in column 1, under "Other Publications", line 43, delete "Architecutres," and insert --Architectures,"--, therefor

On page 4, in column 1, under "Other Publications", line 45, delete "Mangement" and insert -- Management--, therefor

Signed and Sealed this Twenty-ninth Day of March, 2016

Michelle K. Lee

Michelle K. Lee Director of the United States Patent and Trademark Office

CERTIFICATE OF CORRECTION (continued) U.S. Pat. No. 9,189,275 B2

ON THE TITLE PAGE

On page 4, in column 1, under "Other Publications", line 57, delete "Microarchitecture-based" and insert --Microarchitecture-based--, therefor

On page 4, in column 2, under "Other Publications", line 10, delete "retreived" and insert --retrieved--, therefor

On page 4, in column 2, under "Other Publications", line 15-16, delete "Clusters-onDemand," and insert --Clusters-on Demand,--, therefor

On page 4, in column 2, under "Other Publications", line 17, delete "retreived" and insert --retrieved--, therefor

On page 4, in column 2, under "Other Publications", line 23, delete "Toold" and insert --Tool--, therefor

On page 5, in column 1, under "Other Publications", line 47, delete "Offical" and insert --Official--, therefor

IN THE DRAWINGS

Sheet 1 of 10, Fig. 1, insert -- 100--, therefor

IN THE SPECIFICATION

In column 7, line 14, delete "215" and insert --210--, therefor

In column 10, line 63, delete "100." and insert --110.--, therefor

In column 13, line 31, delete "515" and insert --115--, therefor

In column 13, line 56, after "horizontally", insert --.--, therefor

In column 15, line 24, delete "125" and insert --525--, therefor